1. Objectives

The goal of this lab was to implement drivers for the ADC and LCD, create a real-time clock using the SysTick timer, and create an interpreter through a command line. The ADC allows for analog-to-digital conversions, sampling from one of the 12 ADC ports on the board. Setting up the LCD drivers

1. Hardware design (none in this lab)
2. Software Design
   1. A
   2. A
   3. A
   4. A
3. A
4. A
5. A
6. A
7. A
8. A
9. a